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Atty. Dkt. No. 039262-0115

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Tadahiro OHMI et al.
 Title: SEMICONDUCTOR DEVICE AND METHOD OF MANUFACTURING THE SAME
 Application No.: 10/725,063
 Filing Date: 12/02/2003
 Examiner: Pert, Evans T.
 Art Unit: 2826

REPLY TO RESTRICTION REQUIREMENT

Commissioner for Patents
 P.O. Box 1450
 Alexandria, VA 22313-1450

Sir:

In reply to the Office Action mailed October 20, 2005, Applicants hereby elect the claims of Group II, Claims 28, 61-62, 68 and 70-76, drawn to a method with "cleaning", "rinsing" and/or "preparing" of a "semiconductor surface", with "forming an oxide", classified, for example, in class 438, subclasses 758+. Applicants, of course, reserve the right to file a divisional application covering the subject matter of the non-elected claims.

Receipt of the initial Office Action on the merits is awaited.

Respectfully submitted,

Date November 18, 2005

By Phillip J. Articola

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